

WP2816P0020MH

20W RF GaN Power Transistor



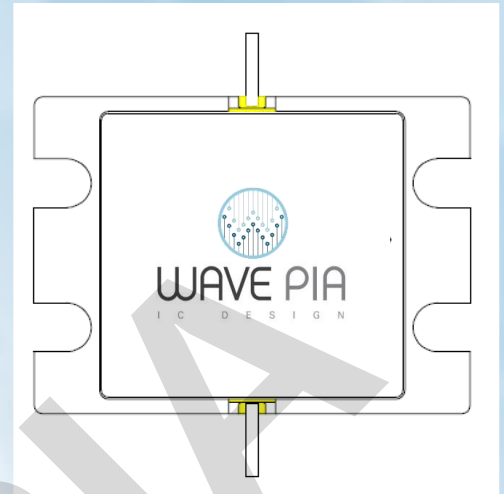
WAVE PIA
I C D E S I G N

Product Features

- Up to 18 GHz Operation
- 9.7dB Small Signal Gain at 16.0 GHz
- 20 W Typical P_{sat} at 16GHz, $V_{DD}=32V$
- 27.3 % Efficiency at P_{sat} at 16 GHz, $V_{DD}=32V$
- 28~32 V Operation

Applications

- Broadband Amplifiers
- Satcom
- Test Instrumentation
- Radar application



Absolute Maximum Ratings

Parameter	Symbol	Rating	Units	Conditions
Drain-Source Voltage	V_{DSS}	160	Volts	25 °C
Gate-to-Source Voltage ³	V_{GS}	-10, +2	Volts	25 °C
Storage Temperature ³	T_{STG}	-65, +150	° C	
Operating Junction Temperature ^{1,3}	T_J	225	° C	
Maximum Forward Gate Current ³	I_{GMAX}	30	mA	25 °C
Maximum Drain Current ²	I_{DMAX}	1	A	$I_d @ V_d = 10V, V_g = 1V$
Soldering Temperature ³	T_S	245	° C	
Storage Temperature ³	T_{STG}	-65, +150	° C	

Note:

1. Continuous use at maximum temperature will affect MTTF.
2. Current limit for long term, reliable operation
3. After additional updates

DC Characteristics¹ (TC = 25 °C)

Parameter	Symbol	MIN	TYP	MAX	Units	Conditions
Gate Threshold Voltage	$V_{GS(th)}$		-3.1		V_{DC}	$V_{DS} = 10\text{ V}, I_D = 1\text{ mA}$
Gate Quiescent Voltage	$V_{GS(Q)}$		-2.2		V_{DC}	$V_{DS} = 28\text{ V}, I_D = 300\text{ mA}$
Saturated Drain Current ²	I_{DS}		1000		mA/mm	$V_{DS} = 10\text{ V}, V_{GS} = 1\text{ V}$
Drain-Source Breakdown Voltage	V_{BR}	160			V_{DC}	$I_D = 1\text{ mA/mm}$

Note:

1. Measured on wafer prior to packaging.
2. Scaled from PCM data.

RF Characteristics (TC = 25 °C, F0 = 16.0 GHz unless otherwise noted)

Parameter	Symbol	MIN	TYP	MAX	Units	Conditions
Small Signal Gain	G_{SS}		9.7		dB	$V_{DD} = 2.8\text{ V}, I_{DQ} = 300\text{ mA}$
Output Power	P_{OUT}		4		W	$V_{DD} = 2.8\text{ V}, I_{DQ} = 300\text{ mA}, \text{Pulse Width} = 100\text{ usec}, \text{Duty Cycle} = 10\%$
Saturated Output Power	P_{SAT}		8		W	$V_{DD} = 2.8\text{ V}, I_{DQ} = 300\text{ mA}, \text{Pulse Width} = 100\text{ usec}, \text{Duty Cycle} = 10\%$
Pulsed Drain Efficiency ¹	η		26.2		%	$V_{DD} = 2.8\text{ V}, I_{DQ} = 300\text{ mA}, \text{Pulse Width} = 100\text{ usec}, \text{Duty Cycle} = 10\% @ P_{sat}$
Output Mismatch Stress	VSWR	-	-	10:1		No damage at all phase angles, $V_{DD} = 2.8\text{ V}, I_{DQ} = 300\text{ mA}, P_{OUT} = 2\text{ W CW}$

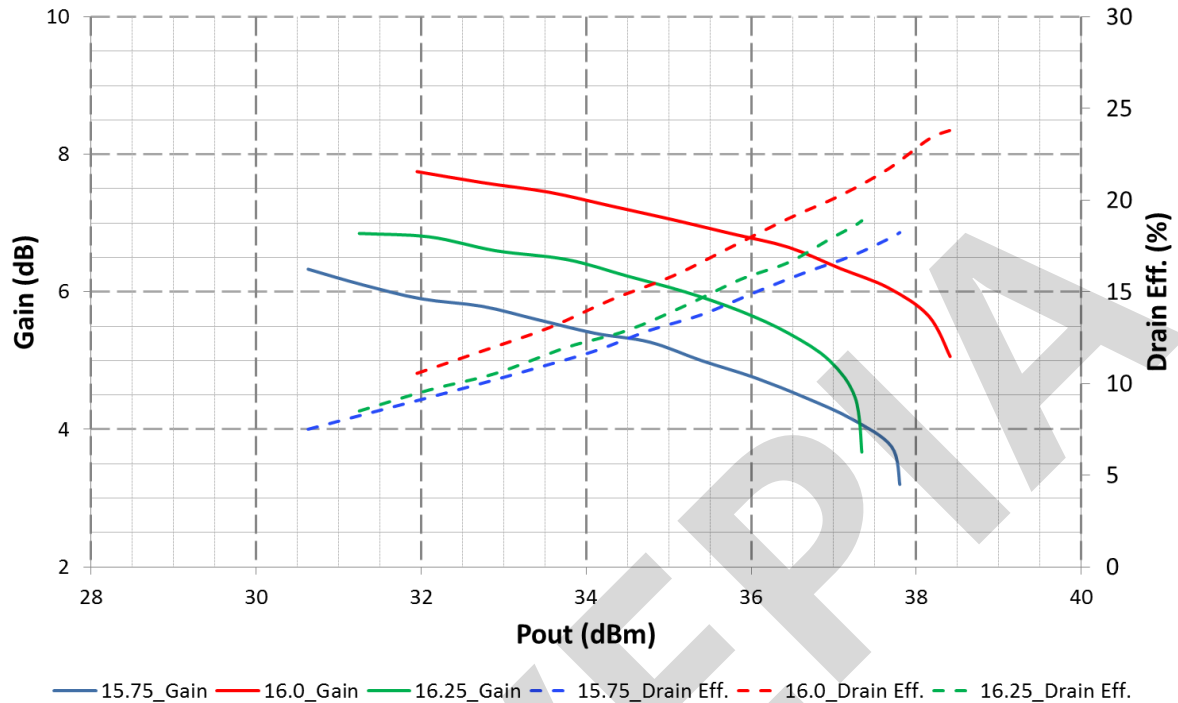
Note:

1. Drain Efficiency = P_{OUT}/P_{DC}

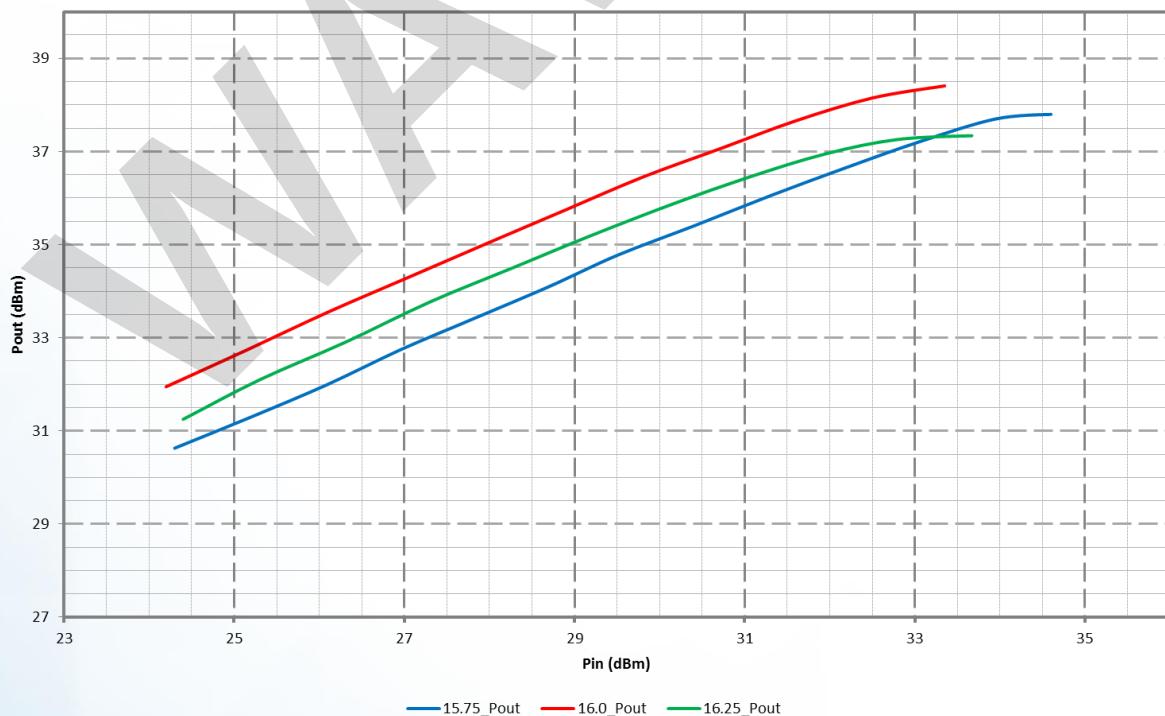
Pulse Signal Performance (Tc=25°C, Measured in the test board amplifier circuit)

VDD = 28V, IDQ = 300 mA, PulseWidth = 100µsec, Duty Cycle = 10%

Gain, Drain Eff. vs. Pout

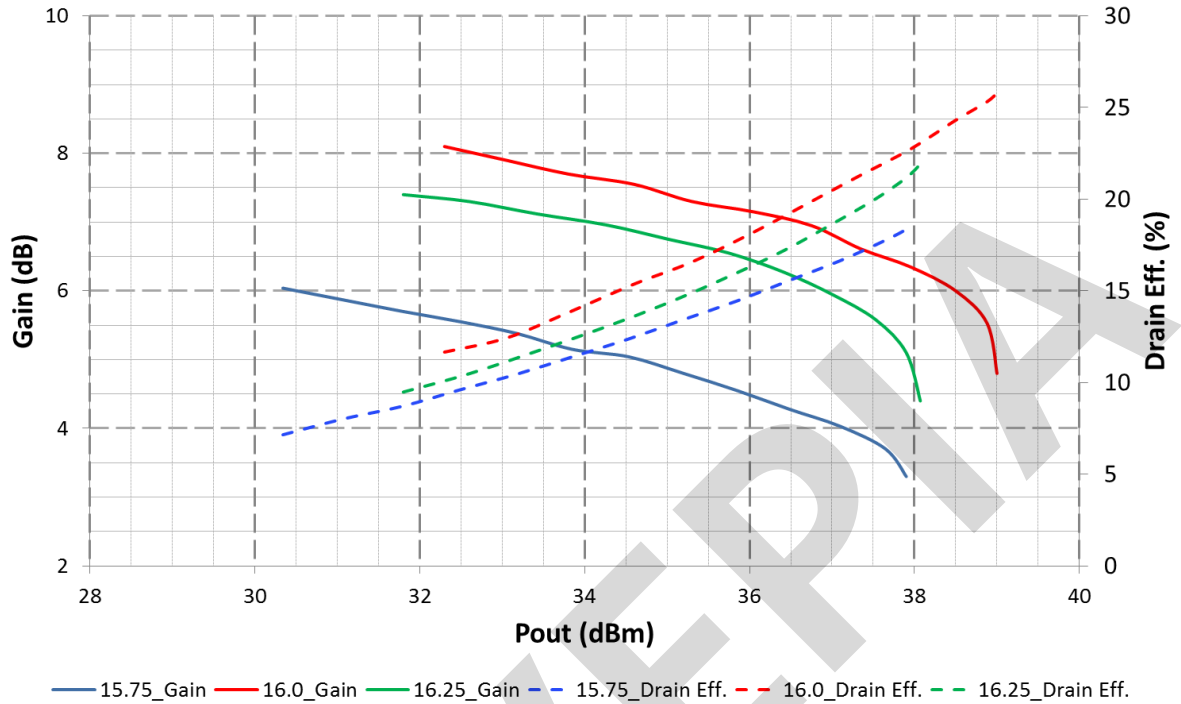


Pout vs. Pin

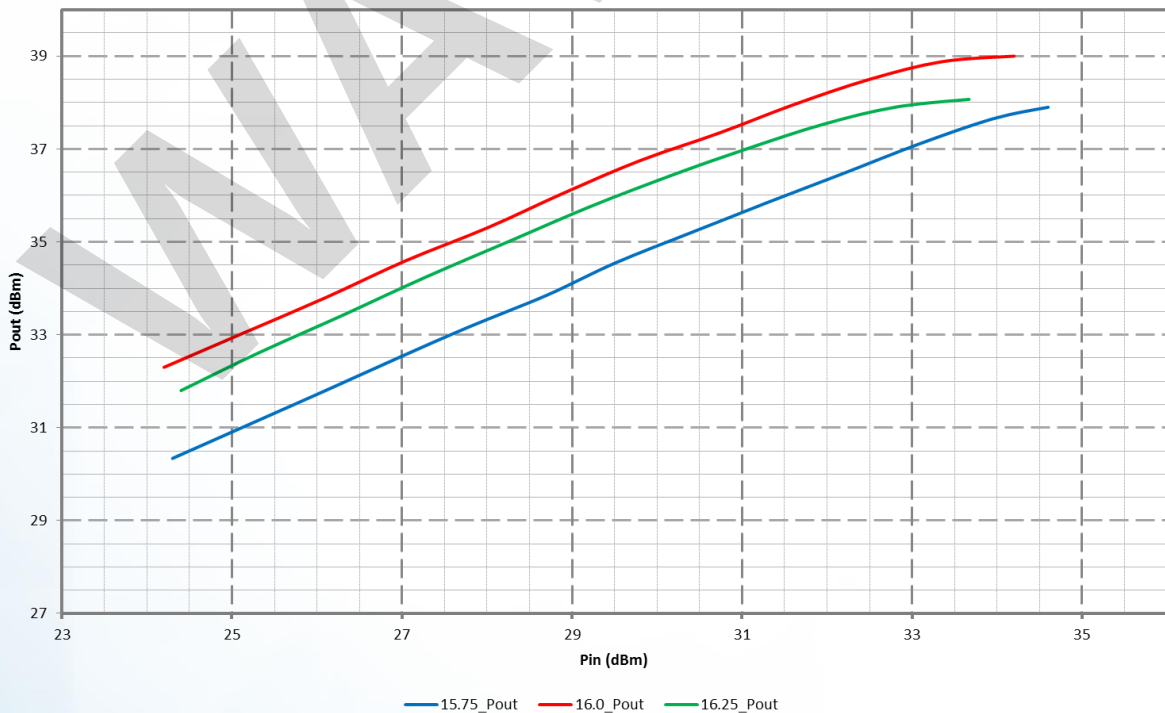


Pulse Signal Performance (Tc=25°C, Measured in the test board amplifier circuit)
VDD = 32V, IDQ = 300 mA, PulseWidth = 100µsec, Duty Cycle = 10%

Gain, Drain Eff. vs. Pout

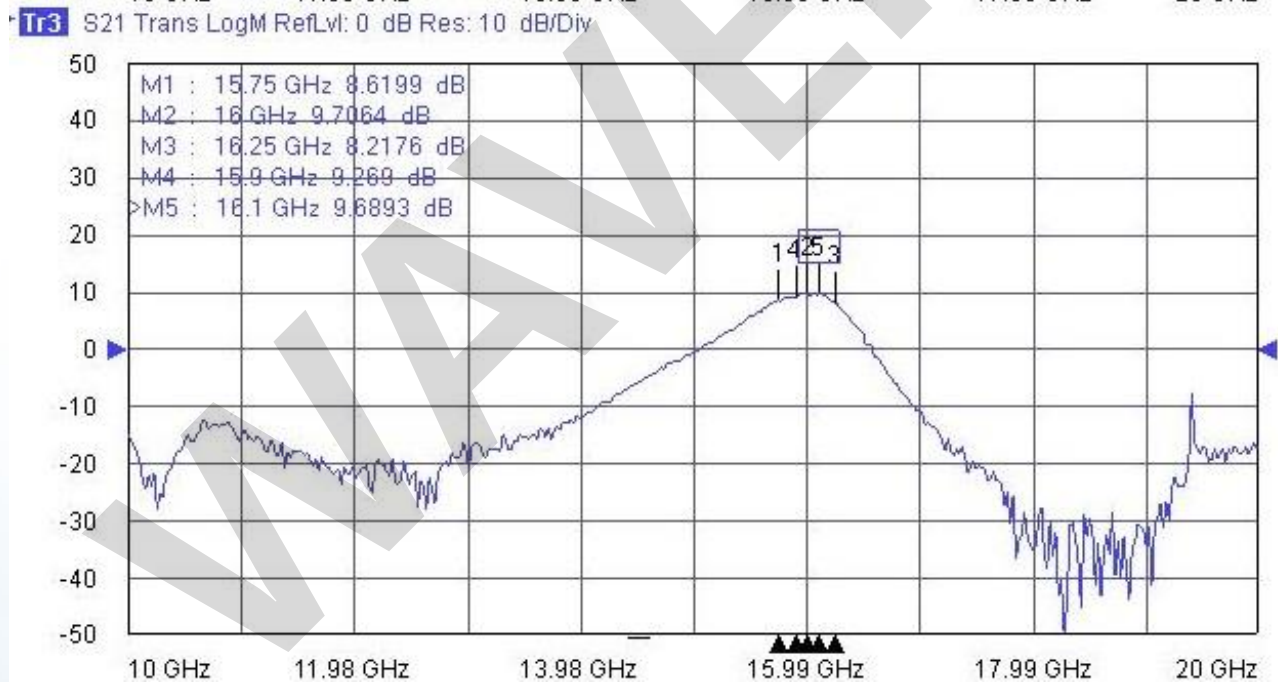
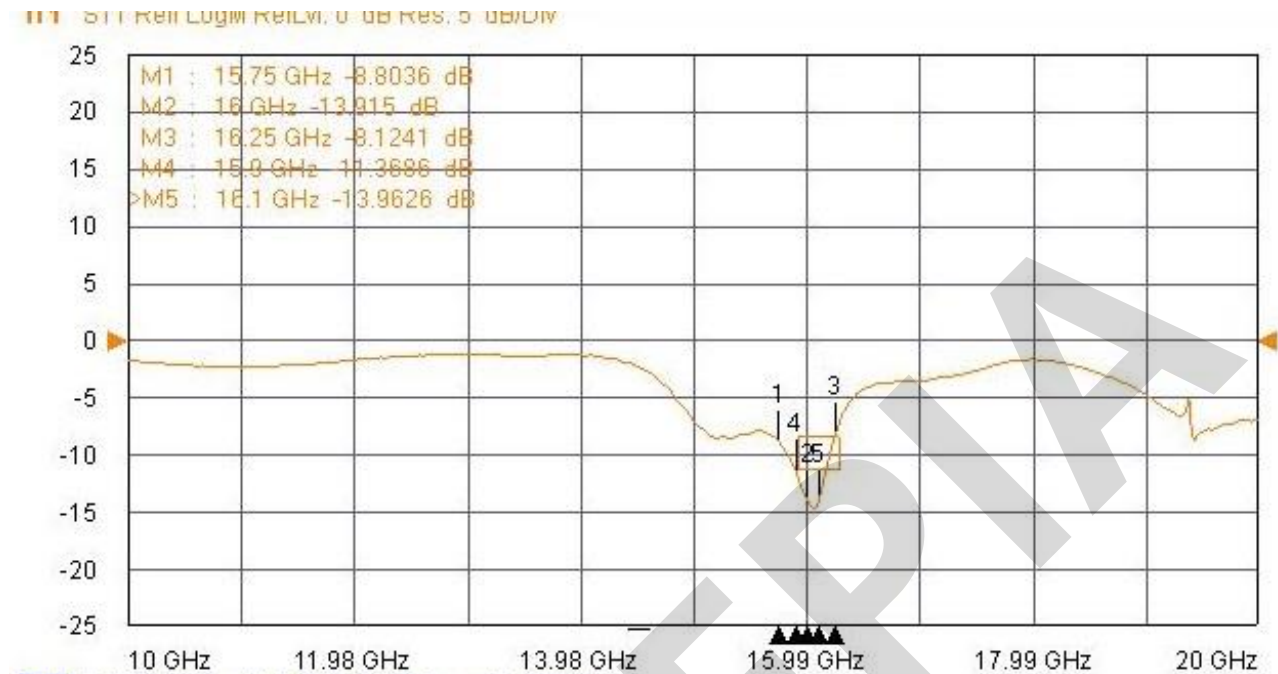


Pout vs. Pin



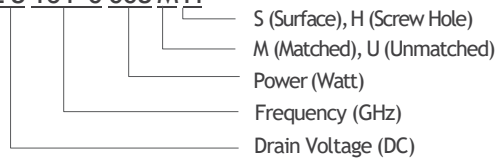
Small Signal Performance (Tc=25°C, Measured in the test board amplifier circuit)

VDD = 28V, IDQ = 300 mA



Part number code

WP 2 8 16 P 0 008 M H



Package Dimensions

